

I. PIN Switching Driver

Model	Channels	Supply Voltage (V)	Input Signal	Output Voltage (V)	Output Current (mA)	Package	Functions
WSD324/ WSD324X	6	±5	TTL	$V_{OH} \geq 4.0$ $V_{OL} \leq -4.0$	±40	C24P/bare chip 4.25*3.51*0.25	Six reverse outputs; with function of detection terminal T
WSD443/ WSD443M	2	+5/-40	TTL	$V_{OH} \geq 2.8$ $V_{OL} \leq -38$	-120	C28P	Dual reverse outputs
WSD591	4	±5	TTL	$V_{OH} \geq 3.2$ $V_{OL} \leq -4.0$	±50	C24P3	Four reverse outputs
WSD591D	4	±5	TTL	$V_{OH} \geq 3.1$ $V_{OL} \leq -4.0$	±60	D16S	Four reverse outputs
WSD595/ WSD595D	4	±5/+30	TTL	$V_{OH} \geq 29$ $V_{OL} \leq -3.5$	+80	C24P/D16S	Four reverse outputs
WSD596	4	±5	TTL	$V_{OH} \geq 4.4$ $V_{OL} \leq -4.4$	±40	C24P/D16S	Four reverse outputs
WSD504	1	+5/-30	TTL	$V_{OH} \geq 2.0$ $V_{OL} \leq -28$	+120	D14S	Single input, complementary output
WSD504-1/ WSD583	1	+5/-50	TTL	$V_{OH} \geq 3.0$ $V_{OL} \leq -47$	+300	D14S	Single input, complementary output
WSD504Y	1	+5/-50	TTL	$V_{OH} \geq 3.0$ $V_{OL} \leq -47$	+300	D14S	Single input, complementary output
WSD529	4	±5	TTL	$V_{OH} \geq 3.4$ $V_{OL} \leq -3.4$	±60	D16S	Four reverse outputs
WSD526	4	±5	TTL	$V_{OH} \geq 3.5$ $V_{OL} \leq -3.5$	±40	FP16	Optional of four in-phase or reverse outputs
WSD537	4	±5	TTL	$V_{OH} \geq 3.5$ $V_{OL} \leq -3.5$	±40	C24P	Optional of four in-phase or reverse outputs
WSD537A	4	±5	TTL	$V_{OH} \geq 3.5$ $V_{OL} \leq -3.5$	±40	Bare chip 1.75*2.05*0.3	Optional of four in-phase or reverse outputs; capability of resisting total dose effects
WSD544	1	+5/-12	CMOS	$V_{OH} \geq 2.0$ $V_{OL} \leq -10$	-100	D14S/C24P	Single input, complementary output
WSD551	1	+8	CMOS	$V_{OH} \geq 7.5$ $V_{OL} \leq 0.1$	150	FP16	In-phase output
WSD570	4	±5	TTL	$V_{OH} \geq 4.0$ $V_{OL} \leq -4.0$	±25	CQFP24	Optional of four in-phase or reverse outputs; adjustable output current
WSD574	2	+20/-5	CMOS	$V_{OH} \geq 19.5$ $V_{OL} \leq -4.0$	+30	FP16	Dual complementary outputs
WSD574X	2	+8/-5	CMOS	$V_{OH} \geq 7.5$ $V_{OL} \leq -4.0$	+30	Bare chip 1.98*2.29*0.25	Dual complementary outputs

PIN Switching Driver (Conti.)

Model	Channels	Supply Voltage (V)	Input Signal	Output Voltage (V)	Output Current (mA)	Package	Functions
WSD574R	2	+8	CMOS	$V_{OH} \geq 7.5$ $V_{OL} \leq 0.5$	-	Bare chip 1.95*2.36*0.30	Dual complementary outputs; can work with a single power supply; capability of resisting single-event effects and total dose effects
WSD574M	2	+9	TTL	$V_{OH} \geq 8.5$ $V_{OL} \leq 0.5$	+30	FP16F	Dual complementary outputs; capability of resisting single-event effects and total dose effects
WSD608/ WSD608X	6	± 5	TTL	$V_{OH} \geq 4.0$ $V_{OL} \leq -4.0$	± 40	C24P/ bare chip 4.46*3.50*0.3	In-phase output; with function of detection terminal T
WSD643	1	+5/-15	TTL	$V_{OH} \geq 4.0$ $V_{OL} \leq -14$	-40	FP08	Single input, complementary output
WSD657	1	± 5	TTL	$V_{OH} \geq 4.0$ $V_{OL} \leq -4.0$	± 80	FP08	Single input, complementary output
WSD667	4	$\pm 5/+36$	TTL	$V_{OH} \geq 34$ $V_{OL} \leq -3.8$	120	D18M	Four reverse outputs; with fault signal detection terminal
WSD303	8	$\pm 5/+27$	≥ 26 ≤ -4.5	$V_{OH} \geq 2.4$ $V_{OL} \leq 0.8$	± 10	D20S	Eight reverse outputs
WSD217	4	$\pm 5/+30$	TTL	$V_{OH} \geq 28$ $V_{OL} \leq -3.8$	120	D18M	Four reverse outputs
WSD637	4	+2.5/-15	TTL	$V_{OH} \geq 1.2$ $V_{OL} \leq -14$	200~270	FP1616-20a	Four in-phase outputs
WSD548	8	+5/+30	TTL	$V_{OH} \geq 4/27$ $V_{OL} \leq 0.5$	+40	D20S	Eight in-phase outputs

II. Ferrite Driver

Model	Channels	Operating Voltage (V)	Input Signal	Driver Output Voltage (V)	Detector Output Voltage (V)	Output Current (mA)	Package	Function
WFD129	2	+15/-6	TTL	$V_{OH} \geq 12.5$ $V_{OL} \leq 1.5$	$V_{OH} \geq 3.5$ $V_{OL} \leq 0.5$	50	D16S	Dual in-phase outputs, with function of mobile phase-shifting
WFD238	2	+9	TTL	$V_{OH} \geq 8.2$ $V_{OL} \leq 0.6$	$V_{OH} \geq 4.5$ $V_{OL} \leq 1.0$	1	LCC24	Dual in-phase outputs, with function of over-frequency protection
WFD243	4	+5/+24	TTL	$V_{OH} \geq 6.5$ $V_{OL} \leq 0.8$	$V_{OH} \geq 3.0$ $V_{OL} \leq 0.8$	-	CQFP64D	Four differential outputs, with functions of over-frequency protection, constant height protection, 5V dropout protection, and output over-voltage protection
WFD244	4	+5/+15	TTL	$V_{OH} \geq 6.5$ $V_{OL} \leq 0.8$	$V_{OH} \geq 3.0$ $V_{OL} \leq 0.8$	-	CQFP64D	Four differential outputs, with functions of over-frequency protection, constant height protection, 5V dropout protection, and output over-voltage protection
WFD248	2	+5/+15	TTL	$V_{OH} \geq 12.5$ $V_{OL} \leq 1.5$	$V_{OH} \geq 2.8$ $V_{OL} \leq 0.5$	± 30	C24P	Dual in-phase outputs, pulse width regulated by DA
WFD285	2	+5/+18	TTL	-	$V_{OH} \geq 2.8$ $V_{OL} \leq 0.8$	800	C28P	Dual in-phase outputs, with function of over-frequency protection
WFD336	1	+5/+9	TTL	$V_{OH} \geq 8.2$ $V_{OL} \leq 0.6$	$V_{OH} \geq 4.5$ $V_{OL} \leq 1$	± 40	C24P	Differential output, peak comparator voltage generated by D/A converter
WFD625A	1	+15~+24	TTL	$V_{OH} \geq 3$ $V_{OL} \leq 0.3$	-	-50	D18S	Differential output, with function of over-frequency protection

III. Low-Voltage Power Modulators

Supply voltage: $\pm 5V$

Model	Channels	Supply Voltage (V)	Input Signal	Output Voltage (V)	Output Current (mA)	Package	Radiation Resistance	Function
WLM126A/ WLM126R	1	+5	TTL	$V_{OH} \geq 3.8$ $V_{OL} \leq 0.3$	≥ 50	D08S	TID $\geq 100k$ LET $\geq 75MeV$	Single reverse output
WLM241	8	+5	TTL	$V_{OH} \geq 1.8$ $V_{OL} \leq 0.4$	≥ 40	LCC28	None	Eight differential outputs
WLM272	1	+5	TTL	$V_{OH} \geq 3.8$ $V_{OL} \leq 0.3$	≥ 250	D08S	None	Single reverse output
WLM358/ WLM358X/ WLM358A/ WLM358R/ WLM358H	4	± 5	TTL	$V_{OH} \geq 4.8$ $V_{OL} \leq -4.8$	≥ 1	D16S/ Bare chip 3.85 *1.70 *0.3	None/ TID $\geq 100k$ LET $\geq 75MeV$	Four differential outputs
WLM301	1	+5	TTL	$V_{OH} \geq 4$ $V_{OL} \leq 0.1$	≥ 300	FP16	None	Single differential output
WLM317	1	+5	TTL	$V_{OH} \geq 3.5$ $V_{OL} \leq 0.5$	≥ 140	DIP14	None	Optional one of two outputs, with function of negative voltage detection
WLM429X	4	+5	TTL	$V_{OH} \geq 4.9$ $V_{OL} \leq 0.1$	-	Bare chip 1.86 *1.84 *0.3	None	Four dual-inputs and gate logic, with function of negative voltage detection
WLM458X	1	± 5	TTL	$V_{OH} \geq -0.1$ $V_{OL} \leq -4.9$	≥ 1	Bare chip 1.30 *1.10 *0.3	None	Single input, complementary output
WLM477R/ WLM560R	2	+5	TTL	$V_{OH} \geq 4.4$ $V_{OL} \leq 0.1$	≥ 100	Bare chip 1.02 *0.98 *0.3	TID $\geq 100k$ LET $\geq 75MeV$	Dual reverse outputs
WLM487X	4	± 5	TTL	$V_{OH} \geq -0.1$ $V_{OL} \leq -4.9$	≥ 1	Bare chip 1.07 *1.07 *0.3	None	Dual inputs, complementary outputs
WLM482X	1	5	TTL	$V_{OH} \geq 4.9$ $V_{OL} \leq 0.1$	-	Bare chip 1.45 *1.33 *0.3	TID $\geq 100k$ LET $\geq 75MeV$	Dual inputs and gate logic, with function of negative voltage detection

Low-Voltage Power Modulators (Conti.)

Supply voltage: $\pm 5V$

Model	Channels	Supply Voltage (V)	Input Signal	Output Voltage (V)	Output Current (mA)	Package	Radiation Resistance	Function
WLM496M	2	+5	TTL	$V_{OH} \geq 4.9$ $V_{OL} \leq 0.1$	-	FP08	None	Dual in-phase outputs, with function of negative voltage detection
WLM927X	2	+3.3 ~+5	TTL	$V_{OH} \geq 2.7/4.4$ $V_{OL} \leq 0.1$	≥ 100	Bare chip 0.89 * 1.09 * 0.3 F08-05	None	Dual reverse outputs
WLM931R	2	+3.3	TTL	$V_{OH} \geq 3.1$ $V_{OL} \leq 0.2$	≥ 60	Bare chip 1.47 * 1.47 * 0.3	TID $\geq 100k$ LET $\geq 80MeV$	Dual in-phase outputs
WLM999R	2	± 5	TTL	$V_{OH} \geq 4.9$	$\geq 150/$ ≥ 700	Bare chip 3.39 * 2.59 * 0.3	TID $\geq 100k$ LET $\geq 75MeV$	Dual in-phase outputs, with function of negative voltage detection
WLM838X/ WLM838E	4	+5	TTL	$V_{OH} \geq 4.7$ $V_{OL} \leq 0.3$	≥ 200	Bare chip 3.96 * 4.36 * 0.3 SOP24L	None	Four reverse outputs

IV. Medium-Voltage Power Modulators

Model	Channels	Operating Voltage (V)	Input Signal	Output Voltage (V)	Output Current (A)	Package	Radiation Resistance	Function
WMM385	1	12	CMOS	$V_{OH} \geq 11$ $V_{OL} \leq 0.5$	≥ 0.2	FP16	None	In-phase output
WMM385R	1	+8	CMOS	$V_{OH} \geq 7.5$ $V_{OL} \leq 0.1$	≥ 0.15	FP16	TID $\geq 60k$	In-phase output
WMM435A	4	12	TTL	$V_{OH} \geq 11.5$ $V_{OL} \leq 0.3$	≥ 0.5	SOP24L	None	Four triple-inputs NAND gate, with enable control port
WMM446X	4	8	CMOS	$V_{OH} \geq V_{DD}-0.4$ $V_{OL} \leq 0.4$	≥ 0.1	Bare chip 3.49 * 3.49 * 0.3	None	Four reverse outputs
WMM472R	2	9	CMOS	$V_{OH} \geq 8.5$ $V_{OL} \leq 0.5$	-	Bare chip 1.95 * 2.36 * 0.3	TID $\geq 100k$ LET $\geq 75MeV$	Dual complementary outputs
WMM930R	1	+9/-3.3	CMOS	$V_{OH} \geq 8.6$ $V_{OL} \leq 0.4$	-	Bare chip 1.10 * 1.52 * 0.3	TID $\geq 100k$ LET $\geq 80MeV$	Reverse output, with function of negative voltage detection
WMM1708	2	10~30	TTL	$V_{OH} \geq V_{DD}-2$ $V_{OL} \leq 0.5$	≥ 3	FP08C	TID $\geq 100k$	Dual in-phase outputs, compliable with UC1708
WMM362	7	50	CMOS	$V_{OH} \geq 48.7$	≥ 0.5	FP16F	None	Darlington transistor, seven reverse outputs
WMM292B	1	7~20	TTL	$V_{OH} \geq 12$ $V_{OL} \leq 2.8$	≥ 2	FP16AW	TID $\geq 100k$	Complementary output
WMM4424	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 3	FP08AC	None	Dual in-phase output, compliable with TC4424
WMM4425	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 3	FP08AC	None	One in-phase output, one reverse output, compliable with TC4425
WMM 4426X/ WMM 4426D/ WMM 4426F/ WMM 4426E	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 1.5	Bare chip 1.90 * 1.94 * 0.3/ D08S/ F08-05/ SOPB	None	Dual reverse outputs, compliable with MAX4426

Medium-Voltage Power Modulators (Conti.)

Model	Channels	Operating Voltage (V)	Input Signal	Output Voltage (V)	Output Current (A)	Package	Radiation Resistance	Function
WMM 4427X/ WMM 4427D/ WMM 4427F/ WMM 4427E	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 1.5	Bare chip 1.90 *1.94 *0.3/ D08S/ F08-05/ SOPB	None	Dual in-phase outputs, compliable with MAX4427
WMM4427M	2	6~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 1.5	F08-05	TID $\geq 20k$ Transient 2E11 rad (si)/s	Dual in-phase outputs, compliable with MAX4427
WMM4427B	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 1.5	F08-03A	TID $\geq 100k$	Dual in-phase outputs, compliable with MAX4427
WMM 4428X/ WMM 4428D/ WMM 4428F/ WMM 4428E	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 1.5	Bare chip 1.90 *1.94 *0.3/ D08S/ F08-05/ SOP8	None	One in-phase output, one reverse output, compliable with MAX4428
WMM4428M	2	4.5~18	TTL	$V_{OH} \geq V_{DD}-0.025$ $V_{OL} \leq 0.025$	≥ 1.5	F08-05	TID $\geq 20k$ Neutron fluence: 2E13n/cm ²	One in-phase output, one reverse output, compliable with MAX4428
WMM321	7	50	CMOS	$V_{OH} \geq 48.7$	≥ 0.5	D16S	None	Darlington transistor, seven reverse outputs
WMM121X	1	5~15	TTL	$V_{OH} \geq 14.5/4.5$ $V_{OL} \leq 0.8$	≥ 0.01	Bare chip 2.98 *1.83 *0.3	None	Hall Decoder